

Overview

The purpose of this notification is to announce that Holt Integrated Circuits has made a Datasheet change to the HI-3582A, HI-3583A, & HI-3584A Product.

Description

Holt Integrated Circuits has added the following:

CLKEN to the timing parameters within the AC ELECTRICAL CHARACTERISTICS Table and the TIMING DIAGRAMS for Receiver Operation.

PLCYC to the timing parameters within the AC ELECTRICAL CHARACTERISTICS Table and the TIMING DIAGRAMS for Transmitter Operation.

This change will result in a data sheet change for HI-3582A, HI-3583A from Revision A to Revision B and for HI-3584A from Revision A to Revision B. The PCN Change type is to Function only. There is no change to Fit, Form, Quality or Reliability.

Reason

It is necessary to ensure that CLK HIGH has a minimum of 25ns separation from second EN pulse HIGH (SEL is HIGH).

It is also necessary to ensure that spacing for PL1 rising to PL2 rising has a maximum of tCLK-10ns.

Implementing CLKEN and PLCYC according to the provided timing parameters will ensure successful operation of the device.

Products Affected

All parts listed are affected by this change. This includes devices in both the standard and Pb-free packages and devices in all grades; industrial, extended temperature and extended temperature with burn-in.

Traceability

None Needed

Qualification Data

None Needed

Response

Note: In accordance with JESD46-C, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this notice.

No response is required. For additional information or questions, please contact:

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Additional Documentation

Below is a list of documents that are associated with this notice:

- o DS3582A & DS3583A Rev. B & DS3584A Rev. B

Revision History

The following table shows the revision history for this document.

Date	Version	Revision Description
5/07/10	1.0	Initial Release – Add CLKEN to Timing Parameters
7/19/10	2.0	Add PLCYC to Timing Parameters